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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/511,868	10/16/2004	Kwok Hong Luk	CN02 0008 US	8915

24738 7590 11/22/2006

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EXAMINER

LUI, DONNA V

ART UNIT PAPER NUMBER

2629

DATE MAILED: 11/22/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/511,868	Applicant(s) LUK, KWOK HONG	
	Examiner Donna V. Lui	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 August 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☒ Claim(s) 1 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 16 October 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|-------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>10/16/2004</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Drawings

2. Figures 3a and 3b should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Objections

3. **Claim 1** is objected to because of the following informalities: Grammatical Errors.

The following is a suggestion from the examiner for correction.

Claim 1, line 7: "the display device, the display parameters belonging to ~~the~~ a group of numbers comprising the number of lines to be displayed, the number of columns to be displayed, where the parameters are related to driving transistors or power saving parameters."

Appropriate correction is required.

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

4. **Claim 1** recites the limitation "the group of number" in line 7. There is insufficient antecedent basis for this limitation in the claim.
5. **Claims 3 and 4** are rejected as failing to define the invention in the manner required by 35 U.S.C. 112, second paragraph.

The claim(s) are narrative in form and replete with indefinite and functional or operational language. The structure which goes to make up the device must be clearly and positively specified. The structure must be organized and correlated in such a manner as to present a complete operative device. The claim(s) must be in one sentence form only. Note the format of the claims in the patent(s) cited.

The following is a suggestion from the examiner.

Claim 3, line 4: steps of programming into said memory means at least one of the parameters: a) the number of lines to be displayed and b) the number of columns to be displayed.

Claim 4, line 3: parameters: e) parameters related to the selection of driving transistors and f) power saving parameters.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

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A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

6. **Claims 1-7** are rejected under 35 U.S.C. 102(b) as being anticipated by Nitta et al. (Pub. No.: US 2001/0004257 A1).

With respect to **Claim 1**, Nitta teaches an electronic apparatus ([0024], lines 3-5; See figure 1, element 1: computer) suitable for displaying information via a display device (element 8: display), the display device having a display panel provided with driving electronics ([0024], lines 5-7; See figure 1, element 9), the electronic apparatus comprising a controller ([0025], lines 2-3; [0026], lines 3-6; [0029], lines 1-3; See figure 1, element 7: graphics card; See figure 2, element 21: ASIC; note that the controller is comprised of the graphics card and ASIC) for selecting at least one application for the display device (note that the selection of at least one application for the display device is equivalent to specification information EDID) and further comprising memory means ([0030], lines 1-5; See figure 2, elements 23 and 25) for storing at least display parameters related to the application and means for ([0037]; See figure 3) providing the display parameters to an interface between the electronic apparatus and the display device, the display parameters belonging to a group of numbers comprising the number of lines to be displayed ([0011], lines 4-8; note that resolution is referred to as a number of pixel columns by the number of pixel rows, thus the number of lines to be displayed is equivalent to the number of pixel rows), the number of columns ([0011], lines 4-8; note that resolution is referred to as a number of pixel columns by the number of pixel rows, thus the number of columns to be displayed is equivalent to the number of pixel columns) to be displayed, where the parameters are

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related to driving transistors ([0026], lines 3-6; note that the display of images is equivalent to driving transistors and that the parameters affect the display of images) or power saving parameters ([0052]; note the existence of a power saving mode, thus the parameters affect the power saving mode and are equivalent to power saving parameters).

With respect to **Claim 3**, Nitta teaches a method for programming a controller for a display device for at least one application for the display device ([0025], lines 2-3; [0026], lines 3-6; [0029], lines 1-3; [0030], lines 1-5; note that the programming of at least one application for the display device is equivalent to specification information EDID outputted from the display to the graphics card; See figure 1, element 7: graphics card; See figure 2, element 21: ASIC; note that the controller is comprised of the graphics card and ASIC) comprising memory means ([0030], lines 1-5; See figure 2, elements 23 and 25) for storing display parameters related to the application the method comprising the steps of programming into the memory means at least one of the parameters: the number of lines to be displayed ([0011], lines 4-8; note that resolution is referred to as a number of pixel columns by a number of pixel rows, thus the number of lines to be displayed is equivalent to the number of pixel rows) and the number of columns to be displayed ([0011], lines 4-8; note that resolution is referred to as a number of pixel columns by the number of pixel rows, thus the number of columns to be displayed is equivalent to the number of pixel columns).

With respect to **Claim 2**, Nitta teaches an electronic apparatus according to claim 1 in which the electronic apparatus further comprises memory means for storing parameters related to

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the selection of driving transistors (*note that depending on the EDID data, such as the resolution, the selection of the number of driving transistors is determined*).

With respect to **Claim 4**, Nitta teaches a method according to claim 3 the method further comprising the steps of programming into the memory means at least one of the parameters: parameters related to the selection of driving transistors ([0026], lines 3-6; *note that the display of images is equivalent to driving transistors and that the parameters affect the display of images*) and power saving parameters ([0052]; *note the existence of a power saving mode, thus the parameters affect the power saving mode and are equivalent to power saving parameters*).

With respect to **Claim 5**, Nitta teaches a method according to claim 3 in which the programming into the memory of the display parameters related to the application is related to a sequence of providing the display parameters to an interface between the electronic apparatus and the display device ([0025]; [0026]; *See figure 1, element 7: graphics card ~ interface, element 8: display ~ display device, and element 1: computer ~ electronic apparatus; note that the display parameters are outputted from the display to the interface which is between the electronic apparatus and the display device*).

With respect to **Claim 6**, Nitta teaches a display device for use in an electronic apparatus according to claim 1, the display device having a display panel provided with driving electronics ([0024], lines 5-7; *See figure 1, element 9*) and means for recognizing an identification code (*See figure 1, element 7; [0026], lines 3-6; note that the means for recognizing an identification code*

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is equivalent to the graphics card) at an interface between the electronic apparatus and the display device ([0011], lines 4-8; note that the serial number of the display apparatus is equivalent to an identification code).

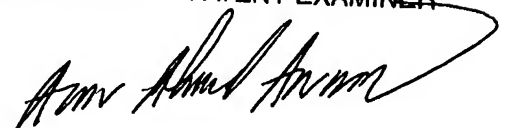
With respect to **Claim 7**, Nitta teaches a display device according to claim 6 the driving electronics further comprising means for storing ([0033]; See figure 2, element 31) in storage means ([0030], lines 1-5; See figure 2, elements 23 and 25) a sequence of parameters controlling the panel ([0011], lines 4-8).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Donna V. Lui whose telephone number is (571) 272-4920. The examiner can normally be reached on Monday through Friday 8:30 a.m. - 5:00 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amr Awad can be reached on (571)272-7764. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

AMR A. AWAD
SUPERVISORY PATENT EXAMINER

A handwritten signature in black ink, appearing to read 'Amr A. Awad', is written over the printed name and title.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Donna V Lui
Examiner
Art Unit 2629

AMP A. AWAD
SUPERVISOR
PATENT EXAMINER

